

WEST Search History



DATE: Friday, August 18, 2006

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
		<i>DB=PGPB,USPT,JPAB; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L33	L31 and (state or space)	18
<input type="checkbox"/>	L32	L31 and intermed\$	13
<input type="checkbox"/>	L31	L29 and (topolog\$ and compil\$)	18
<input type="checkbox"/>	L30	L29 and (topolog\$ same circuit)	1
<input type="checkbox"/>	L29	(runtime or jit) same parameter same (mapping or simulat\$)	181
<input type="checkbox"/>	L28	L23 and (topolog\$ same compil\$)	1
<input type="checkbox"/>	L27	L26 and instruction and match\$	28
<input type="checkbox"/>	L26	L24 and topolog\$	31
<input type="checkbox"/>	L25	L24 and toplog\$	0
<input type="checkbox"/>	L24	L23 and compil\$ and context	79
<input type="checkbox"/>	L23	L22 and (flow\$3 same graph) and (data same structure)	101
<input type="checkbox"/>	L22	runtime same paramet\$ and (mapping or simulat\$)	1400
<input type="checkbox"/>	L21	L1 and (flow\$4 adj graph) and (data same structure)	31
<input type="checkbox"/>	L20	L2 and (flow\$4 adj graph) and (data same structure)	8
<input type="checkbox"/>	L19	L2 and (flow\$4 adj graph) and compil\$ and context	8
<input type="checkbox"/>	L18	L17 and (code and (source or object))	8
<input type="checkbox"/>	L17	L16 and simulat\$4	8
<input type="checkbox"/>	L16	L15 and (templat\$ or library or platform)	8
<input type="checkbox"/>	L15	L14 and (head\$4)	10
<input type="checkbox"/>	L14	L6 and vera	33
<input type="checkbox"/>	L13	L12 and (precompil\$ or preprocess\$ or preselect\$)	1
<input type="checkbox"/>	L12	L8 and task and run	21
<input type="checkbox"/>	L11	L10 and (conte\$ same select\$)	11
<input type="checkbox"/>	L10	l8 and (include\$3 same file)	23
<input type="checkbox"/>	L9	l7 and L8	3
<input type="checkbox"/>	L8	L6 and (head\$ same flag\$)	26
<input type="checkbox"/>	L7	L6 and vera and head\$	10
<input type="checkbox"/>	L6	testbench\$ or (test adj bench\$)	2246
<input type="checkbox"/>	L5	L3 and (compil\$ and simulat\$)	17
<input type="checkbox"/>	L4	L3 and (compil\$ or simulat\$)	28
<input type="checkbox"/>	L3	L1 and (flow adj graph) and intermediate	29

<input type="checkbox"/>	L2	L1 and tree and interm\$	228
<input type="checkbox"/>	L1	runtime same mapping	955

END OF SEARCH HISTORY